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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Yuichi SATO

09/412,328 Serial No.:

Filing Date: October 5, 1999

STATIC RANDOM ACCESS MEMORY AND SEMICONDUCTOR DEVICE USING MOS TRANSISTORS HAVING CHANNEL REGION ELECTRICALLY CONNECTED WITH GATE

Examiner: Douglas A. Wille

Group Art Unit: 2814

## RESPONSE UNDER 37 CFR 1.111

Commissioner for Patents Washington, D.C. 20231

Sir:

In response to the Office Action dated June 7, 2002, the following remarks are respectfully submitted. Reconsideration of this application is hereby requested.